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a conductive wire comprising at least a first contact end and a second contact end, the first contact end electrically connecting with a gate electrode on the top of the MOS transistor, and the second contact end  
5 electrically connecting with a doped region in the substrate; and

a fusion area positioned in the conductive wire to disconnect the conductive wire and the MOS transistor, the fusion area comprising polysilicon;  
10 wherein ions in the gate oxide are transmitted to the doped region via the conductive wire so as to reduce plasma damage to the gate oxide.

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Claim 4 is canceled.

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